

PROGRAM SCHEDULE:

Time (hrs)	Activity
09:00 to 10:00	Registration
10:00 to 10:30	Inauguration
10:30 to 10:45	Tea Break
10:45 to 12:15	Experts Talk
12:15 to 13:30	Experts Talk
13:30 to 14:00	Lunch Break
14:00 to 14:30	Experts Talk
14:30 to 15:30	Panel discussion & valedictory function

COURSE FEE AND REGISTRATION:

1. Participants from AICTE/UGC approved institutions: Rs. 300/- per Participant.

The course fee includes course material, lunch, tea and snacks.

The amount can be paid by “DD/Cheque” in favor of “The Principal, Terna Engineering College, Nerul” payable at *Navi Mumbai*.

For more details about registration contact:

Mr. V.S. Chede (9820471724)

Email: vinodchede@rediffmail.com

Mr. D.S. Kurule (9322263426)

Email: deepak_kurule@rediffmail.com

Mr. A.G.Gaikwad (9222189830)

Email: abhijit2727@yahoo.com

Registration form is also available online at website

www.terna.org

CHIEF PATRON:

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PATRONS:

Mr. Ashok Aher, Trustee, TPCT

Mr. P. T. Deshmukh, CEO, TPCT

Dr. K. T. V. Reddy, Principal, TEC

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Board of studies of Electronics

Prof. B.G.Hogade, Member, Board of studies of Electrical Engineering, University of Mumbai.

Prof.S.S.Shinde, Controller of Examination, University of Mumbai.

EXPERT SPEAKERS:

Mr. K. K. Minocha

G.M. BSNL,

Prof. S.S. Thakur

Chairman, Board of studies of E & T

University of Mumbai.

Dr.Gulnar Sharma

Director, J.B. Institute of Management.

S.N.D.T.

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Mr. D.S. Kurule

Mr. A.G. Gaikwad

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Mr. D.S. Bhosale

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Mr. S.B. Karande

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TPCT'S

Terna Engineering College, Nerul
&

University of Mumbai

Jointly organizes

Subject Orientation programme for
Electronic and Telecommunication
Engineering Faculties of Mumbai
University

On

**“Industrial Economics &
Telecommunication Regulation”**

(20th Jan. 2010)

Convener

Mr.V.S.Chede

Department of Electronics &
Telecommunication Engineering

Terna Engineering College

Sector 22, Phase II, Nerul (W)

Navi Mumbai 400706.

Phone no. 022-27718134,

Fax -02227722978

e-mail id: vinodchede@rediffmail.com

www.terna.org

ABOUT THE COLLEGE:

Terna Engineering College (TEC) was established in 1991 by **Terna Public Charitable Trust** with a vision to establish a technical institute of excellence. The institute, “**Graded A**” by DTE, Govt. of Maharashtra, is affiliated to the University of Mumbai and approved by All India Council for Technical Education (AICTE). We are in the process of NBA accreditation. The institute is also “ISO 9001: 2000” certified for its academic services. It is one of the few engineering colleges in India to achieve this international quality standard.

ABOUT THE DEPARTMENT:

The Department of Electronics & Telecommunication Engineering has full time UG Course with intake of 90 students and full time PG course with intake of 18. We have team of Twenty Six dedicated, experienced and highly motivated teaching faculties. In addition, we have well equipped laboratories to facilitate the staff and students to carryout the R&D work. Experts from Industry and academic institutes are frequently invited to deliver special talks on latest areas of R&D and the innovative technologies. This in turn helps the staff and students to enrich their knowledge.

COURSE OBJECTIVE:

For making the students aware of the latest developments, Mumbai University periodically changes the syllabus/ curriculum. In this academic year 2009-10 it has made substantial changes in the syllabus of third year electronics & telecommunication. So to introduce the faculties of Mumbai University with new syllabus of third year Sem-VI subject Industrial Economics & Telecommunication Regulation, we at Terna engineering college, jointly in association with Mumbai University are organizing “Orientation Programme” for the faculties. Faculties will be guided about the scheme of syllabus, by the team of experts who will help them to meet all the demands connected with theory and tutorial of above mentioned subject.

COURSE DESCRIPTION:

The objective of this course is to introduce to the students the basic concepts of Economics & Management and give them exposure to Telecommunication Regulation in India/ in general. The subject is divided mainly in three parts firstly Economics then Banking & Taxation system of India, And Telecommunication Regulation. An emphasis is also to be given on International Trade and Economic Crises of 2008.

(Photocopy of enclosed form is also acceptable)

Last Date of Registration: 18th Jan. 2010.
Orientation programme for
“Electronics & Telecommunication
Engineering Faculties of Mumbai University”
20th Jan. 2010

Full Name: _____

Designation: _____

Qualification: _____

Experience: _____

Institution/Organization. _____

Mailing Address: _____

Phone (O) _____ (R) _____

Email: _____

Please find enclosed Cheque/ DD No.: _____

Dated ___/___/___ is drawn in favour of

"The Principal, TEC, Nerul" payable at **"Navi Mumbai"**

drawn at Bank _____

Signature of the Applicant

Date & Place:

Signature of the forwarding Authority

With seal

Date & Place:

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14:00 to 15:00	Panel discussion And Practical Demonstration
15:00 to 15:15	Tea Break
15:15 to 16:00	Valedictory Function

Online registration form is also available on website
www.terna.org

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Hon. Dr. Padmasinha Patil, Chairman

PATRONS:

Mr. Ashok Aher, Trustee, TPCT

Mr. P.T.Deshmukh, CEO, TPCT

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ADVISORY BOARD:

Mr.Amol Chavan, MC, TEC

Mr.S.S.Sahare, Dean, Faculty of Technology

Mr.S.S.Thakur, Chairman, Board of Studies

Mr. B. G. Hogade,Member, Board of Studies

Mr. R.B.Randale,Seceretary,ISTE,TEC

EXPERTS:

Mr.Ramesh D.Moon (AE, MAHAGENCO)

Mr. Shirish S. Joshi (Director, ADM)

COORDINATORS:

Mrs. Kanchan S. Gorde

Mrs. Swati Sinha

ORGANIZING COMMITTEE:

Ms. Alka Khade

Mr. S.D.Nandgaonkar

Ms. S.B.Gaikwad

Mr. D.B.Patil

Mr. M.M.Jadhav

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 Knowledge to Empower



TPCT's

Terna Engineering College, Nerul

&

University of Mumbai

Jointly Organizes

**“Orientation Program for Electronics
 Engineering Faculties of Mumbai
 University”**

on

Microprocessor&MicroController-II

(January 28, 2010)

Convener

Mr. M.B.Limkar

Department of Electronics Engg.

Terna Engineering College

Sector 22, Phase II, Nerul (W)

Navi Mumbai 400706.

Phone no. 022-27718134

Fax - 022-772978

e-mail id : hod_el@terna.org

www.terna.org

ABOUT THE COLLEGE

Terna Engineering College (TEC) was established in 1991 by Terna Public Charitable Trust with a vision to establish a technical institute of excellence. The institute, "Graded A" by DTE, is affiliated to the University of Mumbai and approved by All India Council for Technical Education (AICTE). The institute is also "ISO 9001: 2000" certified for its academic services. It is one of few engineering colleges in India to achieve this international quality standard. We are in process of accreditation.

ABOUT THE DEPARTMENT

The Department of Electronics Engineering aims at bridging the gap between the vast resources of information and the student's eagerness to acquire knowledge. The faculty forms the nerve of the department. We have a well trained and highly motivating teaching staff. In addition to the teaching assignments, some faculty members are engaged in research work which is actively supported by the college. Stalwarts from the industry are frequently invited to conduct seminars on topics on various subjects.

COURSE OBJECTIVE

For making the students aware of the latest technologies, Mumbai University periodically changes the syllabus /curriculum. In the academic Year 2009-2010, it has made substantial changes in the syllabus of Third Year Electronics. So, to introduce the faculties of Mumbai University with new syllabus of Third Year subject : Microcontroller and Microprocessor-II, we at Terna Engineering college, jointly in association with Mumbai University are organizing "Orientation Program" for the faculties. Faculties will be guided about the scheme of syllabus, by the team of experts who will help them to meet all the demands connected with theory and practical of above mentioned subject.

NOTE: In Practical Demonstration the topics covered will be:

1. University Suggested Experiments.
2. Interfacing of various Peripherals to Microprocessor and Microcontroller-II.
3. How to use MICROCHIP (PIC) Compiler and Built-in Programming software for ISP application.
4. Industrial Examples of Microprocessor and Microcontroller-II

COURSE FEE AND REGISTRATION:

1. Participants from AICTE/UGC approved Institutions: Rs.100/- per participant.

The amount can be paid by DD in favor of "The Principal, TEC, Nerul." payable at *Navi Mumbai*. The course fee includes course material, working day lunch, tea and snacks.

For more details about registration contact:

(i) **Mrs. Kanchan S.Gorde** (Mob:-9769227264)
Email ID: gordekanchan@yahoo.co.in

(ii) **Mrs. Swati Sinha** (Mob:-9322097594)
Email ID: swatisinha2005@rediffmail.com

(Photocopy of enclosed form is acceptable)
Last Date of Registration: January 28, 2010

"Orientation Program for Electronics Engineering Faculties of Mumbai University" January 28, 2010

Full Name: _____

Designation: _____

Qualification: _____

Experience: _____

Institution/Organization: _____

Mailing Address: _____

Phone (O) _____ (R) _____

Email: _____

Please find enclosed Cheque/ DD No.: _____

Dated ___/___/___ is drawn in favour of

"The Principal, TEC, Nerul" payable at "Navi
Mumbai" drawn at Bank _____

Signature of the Applicant

Date & Place:

Signature of the forwarding Authority

With seal

Date & Place: