

University of Mumbai



(466)
SECOND HALF 2011
(Unique Exam Code 51102006)

PROGRAMME OF THE M.E. (ELECTRONICS ENGG.) (SEM II) EXAMINATION

Candidates for the above examination are requested to be in attendance at the place of examination, fifteen minutes before the time appointed for setting of the first paper and ten minutes before the time fixed for setting of each subsequent paper.

THEY ARE FORBIDDEN TO TAKE ANY BOOK OR PAPER INTO THE EXAMINATION HALL.

Seat numbers and places of examination will be announced on the college notice boards four days prior to the date of commencement of the examination.

Smoking is strictly prohibited in the examination hall.

The written examination will be conducted in the following order :-

Days and Dates	Time	Paper
Tuesday, November 29, 2011	10:30 a.m. to 01:30 p.m.	Microprocessor and Systems – II.
Saturday, December 03, 2011	10:30 a.m. to 01:30 p.m.	VLSI Design.
Friday, December 09, 2011	10:30 a.m. to 02:30 p.m.	Elective III/IV – Power Electronics.
Wednesday, December 14, 2011	10:30 a.m. to 02:30 p.m.	Elective III/IV – Modern Digital Signal Processing.
Monday, December 19, 2011	10:30 a.m. to 02:30 p.m.	Elective III/IV – Advanced Communication Theory.
Friday, December 23, 2011	10:30 a.m. to 01:30 p.m.	Process Instrumentation and Controller Design.

Mumbai - 400 098
26th September, 2011

Prof. Vilas B. Shinde
Controller of Examinations